

PATENT NUMBER

U.S. UTILITY Patent Application

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APPLICATION NO. 09/888017	CONT/PRIOR D	CLASS 438 118	SUBCLASS 719	ART UNIT 2812	EXAMINER 105
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APPLICANTS

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Задания

High throughput architecture for semiconductor processing

PTO-2040
12/99

ISSUING CLASSIFICATION

ORIGINAL		CROSS REFERENCE(S)					
CLASS	SUBCLASS	CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)				
INTERNATIONAL CLASSIFICATION							

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<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drawn	Figs. Drawn	Print Figs.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed. <input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent No. _____ <input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	_____ (Assistant Examiner) _____ (Date) _____ (Primary Examiner) _____ (Date) _____ (Legal Instruments Examiner) _____ (Date)			NOTICE OF ALLOWANCE MAILED	
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